



Sheet 1 of 4

FORM PTO-1449 U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE LIST OF REFERENCES CITED BY APPLICANT (Use several sheets if necessary)	ATTY. DOCKET NO.	SERIAL NO.
	SON-2234	09/978,058
	APPLICANT	
	Toru TAKESHITA et al.	
FILING DATE	GROUP	
October 17, 2001	2633	

U.S. PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NO.	DATE	NAME	CLASS	SUB- CLASS	FILING DATE
TD	AA	6,081,572	6/00	FILIP			
TD	AB	5,889,828	3/99	MIYASHITA			
	AC						
	AD						
	AE						
	AF						

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FOREIGN PATENT DOCUMENTS

		DOCUMENT NO.	DATE	COUNTRY	CLASS	SUB- CLASS	TRANSLATION YES NO PART.		
	AG								
	AH								
	AI								
	AJ								
	AK								
	AL								

OTHER REFERENCES (Including Author, Title, Date, Pertinent Pages, Etc.)

TD	AM	Hui Wang and Richard Nottenburg: XP-000862367; A 1Gb/s CMOS Clock and Data Recovery Circuit; dated February 1999, Vol.42, pages 354-355.
TD	AN	Hui Wang and Richard Nottenburg: A CMOS Low-jitter Phase Frequency Detector for Giga-bit/s Clock Recovery; dated July 1999; pages 91-93.
TD	AO	T.H. Toifi and P. Moreira: Simple frequency detect or circuit for biphase and NRZ clock recovery; dated October 1, 1998 Vol. 34, No. 20; pages 1922-1923.

EXAMINER Dung Tran	DATE CONSIDERED 8/25/2004
*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.	